



SWARNANDHRA COLLEGE OF ENGINEERING AND TECHNOLOGY

(Autonomous)

Narsapur, West Godavari District, A.P. 534280

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

BoS Meeting

Dt.:23.10.2025

The Board of Studies (BoS) meeting of the ECE department held on 23.10.2025 at 10.30 A.M. through online mode for the review and approval of R -25 M.Tech (VLSI&SD), I and II SEMS course structure and syllabus.

The following members are attended the meeting

S. No.	Name of the Member	Designation in BoS
1.	Dr. B. Subrahmanyeswara Rao	Chairman
2.	Dr. N.N. Murthy, Dean, IIT, Tirupathi	Subject Expert
3.	Dr. K. Bala Murugan, Professor, Savitha University, Chennai	Subject Expert
4.	Mr. V. Govinda Rao, Microlink Technologies, Vijayawada	Industrial Expert
5.	Ms. K. Madhu Harshitha, Software Associate, CTS, Chennai	Alumni Member
6.	Dr. Y.S.V. Raman	Member
7.	Dr. B. Ramana Kumar	Member
8.	Dr. D. Nataraj	Member
9.	Mr. J.E.N. Abhilash	Member
10.	Mr. D. Sekhar	Member
11.	Mrs. G.B. Christina	Member

Agenda:

Review & Approval of R -25 M.Tech (VLSI&SD), I and II SEMS Course structure and syllabus.





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MINUTES OF BoS MEETING

1. BoS Chairman welcomed the University Nominee Dr. N.N. Murthy, Subject Expert Dr. K. Bala Murugan Dr.N.N. Murthy,Industrial Expert Mr. V. Govinda Rao, Alumni Member Ms. K. Madhu Harshitha and Faculty members.
2. BoS Chairman presented the above agenda points and later room is given to the experts for discussion and suggestions.

Dr. N.N. Murthy,Dean, IIT, Tirupathi University Nominee Suggested the following:

- 1) To follow the JNTU Kakinada course structure and syllabus of latest regulation.
- 2) To modify the only 20% of syllabus of any course.

Dr. K. Bala Murugan,Professor,Savitha University,Chennai, Subject Expert suggested the following:

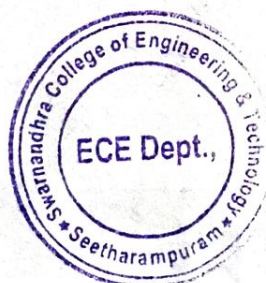
- 1) To add the advanced topics in required courses.
- 2) Given approval for course structure and syllabus.

Mr. V. Govinda Rao, Microlink Technologies, Vijayawada, Industrial Expert:

- 1) Suggested to include syllabus which is useful for latest VLSI design and interfaces used for Project development.
- 2) Suggested to include few more design experiments in laboratory courses.

Ms. K. Madhu Harshitha, Software Associate, CTS, Chennai, Alumni Suggested.

- 1) Suggested to add the semiconductor industry related topics.



Dr. B. S. Rao
20/10/26
Dr. B. S. Rao
Chairman BoS,
Department of ECE